

Listing of the Claims:

The following is a complete listing of all the claims in the application, with an indication of the status of each:

1 1 (Currently Amended). An apparatus for the transmission of
2 time-synchronous data from a sender to a receiver using a network, wherein
3 the time-synchronous data is processed and transmitted at the sender as well as
4 the receiver, the mechanism comprising:

5 a first processing unit composed of multiple subcomponents, each
6 subcomponent being designed to process the time-synchronous data in a
7 specific and different way;

8 and a second processing unit parallel to the first processing unit, said
9 second processing unit being composed of multiple subcomponents, each
10 subcomponent being designed to process the time-synchronous data in a
11 specific and different way, wherein the subcomponents of the second
12 processing unit is are setup and/or adapted based on changed sender data rate
13 or network characteristics by configuring attribute parameters of the
14 subcomponents, wherein data processing and transmission of the time-
15 synchronous data is continued within the first processing unit during the setup
16 and adaption adaptation of the second processing unit; and

17 a switch selecting between the first and second processing units, the
18 processing and transmission of the time-synchronous data initially being
19 performed by the first processing unit and, after switching by the switch, the
20 processing and transmission of the time-synchronous data is performed using
21 the second processing unit such that the processing and transmission of the
22 time-synchronous data is performed within the second processing unit.

1 2 (Currently Amended). The apparatus according to claim 1, wherein the setup
2 and/or adaptation of the second processing is started using a trigger event.

1 3 (Previously Presented). The apparatus according to claim 1, wherein the
2 switching is performed after completion of the setup and adaptation of the
3 second processing unit.

1 4 (Previously Presented). The apparatus according to claim 1, wherein the
2 switching is performed after reaching a certain switching condition.

1 5 (Previously Presented). The apparatus according to claim 4, wherein the
2 certain switching condition is whether at least one given parameter reaches at
3 a predetermined value.

1 6 (Previously Presented). The apparatus according to claim 1, wherein the
2 time-synchronous data is processed in the first processing unit using a
3 plurality of subcomponents.

1 7 (Previously Presented). The apparatus according to claim 6, wherein the
2 subcomponents include at least one of a codec, a filter, a packetizer, and a
3 memory buffer.

1 8 (Previously Presented). The apparatus according to claim 1, wherein the
2 time-synchronous data is processed in the second processing unit using a
3 plurality of subcomponents.

1 9 (Previously Presented). The apparatus according to claim 8, wherein the
2 subcomponents include at least one of a codec, a filter, a packetizer, and a
3 memory buffer.

1 10 (Previously Presented). The apparatus according to one claim 8, wherein
2 the subcomponents are connected during setup.

1 11 (Previously Presented). The apparatus according to claim 1, wherein the
2 first and second processing unit is initialized after setup.

1 12 (Previously Presented). The apparatus according to claim 8, wherein each
2 of the subcomponents of the second processing unit is adapted to the other
3 subcomponents or changed sender data rate or changed network
4 characteristics.

1 13 (Previously Presented). The apparatus according to claim 6, wherein, after
2 switching by the switch, the subcomponents of the first processing unit are
3 de-attached from each other.

1 14 (Previously Presented). The apparatus according to claim 13, wherein a
2 plurality of the second processing units is setup and, after switching by the
3 switch, the subcomponents of the first processing unit are included in one of
4 the second processing units.

1 15 (Previously Presented). The apparatus according to claim 6, wherein after
2 switching by the switch, the subcomponents of the first processing unit remain
3 connected.

1 16 (Currently Amended). The apparatus according to claim 1, wherein a
2 plurality of second processing units are setup and/or adapted based on changed
3 data load rate and network characteristics.

1 17 (Previously Presented). The apparatus according to claim 1, wherein an
2 additional processing unit for the processing and transmission of time-
3 synchronous data is used in sequence with the first and second processing
4 units.

1 18 (Previously Presented). The apparatus according to claim 1, wherein the
2 time-synchronous data is gathered with one of mechanisms for acquiring
3 visual data and speech data.